

ABSTRACT OF THE DISCLOSURE

A method and device for driving a plasma display panel is provided in which a drop of an increasing voltage rate due to discharge is prevented, and a reset period is shortened. In driving a plasma display panel by applying an increasing voltage to cells of a display screen during a reset period for equalizing charge of the cells, an increasing voltage signal is supplied to an impedance conversion circuit in which an output impedance is lower than an input impedance, and the output signal of the impedance conversion circuit is supplied to the cells.

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